



#2
1-18-01

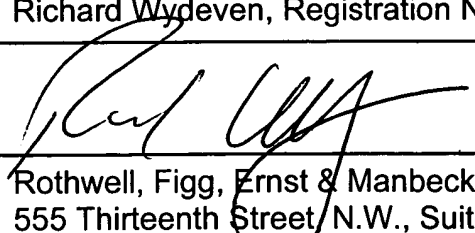
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	<i>Application Number</i>	Divisional of Serial No. 09/499,042 filed February 7, 2000
	<i>Filing Date</i>	December 3, 2001
	<i>First Named Inventor</i>	Todd MCNEEL et al.
	<i>Group Art Unit</i>	Unknown
	<i>Examiner Name</i>	Unknown
	<i>Attorney Docket Number</i>	914-1412-DV2
	<i>Today's Date</i>	December 3, 2001
<i>Title of the Invention:</i> APPARATUS AND METHOD FOR MAKING STACKABLE TORTILLA CHIPS		

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The material listed on the accompanying forms PTO-1449 and form PTO-892, from the parent application having U.S. serial Number 09/499,042, is cited in compliance with the provisions of 37 C.F.R. §§ 1.56, 1.97 and 1.98. Applicants respectfully request that the Examiner consider these references with respect to the present application. Copies of these references can be found with the parent application and, accordingly, will not be resubmitted unless requested by the Examiner.

RESPECTFULLY SUBMITTED,					
<i>NAME AND REG. NUMBER</i>	Richard Wydeven, Registration No. 39,881				
<i>SIGNATURE</i>				<i>DATE</i>	12/03/2001
<i>ADDRESS</i>	Rothwell, Figg, Ernst & Manbeck, P.C. 555 Thirteenth Street, N.W., Suite 701, East Tower				
<i>CITY</i>	Washington	<i>STATE</i>	D.C.	<i>ZIP CODE</i>	20004
<i>COUNTRY</i>	U.S.A.	<i>TELEPHONE</i>	(202) 783-6040	<i>FAX</i>	(202) 783-6031

914-14121DS

Attachments: Form PTO/SB/08 and form PTO-892